IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Paul Elliott
Application No. : 10/658,595

Filed : September 9, 2003

For : BRIDGE CIRCUIT FOR USE IN RETIMING IN A

SEMICONDUCTOR INTEGRATED CIRCUIT

Examiner : Nimesh G. Patel

Art Unit : 2112

Docket No. : 851963.413

Date : September 21, 2006

Mail Stop Amendment Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

RESPONSE

Commissioner for Patents:

In response to the Office Action dated March 21, 2006, please extend the period of time for response 3 months, to expire on September 21, 2006. Enclosed are a Petition for an Extension of Time and the requisite fee. Please reconsider the application in view of the following remarks.

A listing of claims begins on page 2 of this paper for the Examiner's convenience.

Remarks begin on page 8 of this paper.